

ABSTRACT OF THE DISCLOSURE

A phase correction circuit includes a phase control data generation circuit which judges whether or not the present sector is a leading sector in an ECC block, which is the minimum recording unit in a magneto-optical disk, and generates phase control data by referring to a phase difference data of a previous sector when the present sector is not the leading sector, based on a phase difference detected by a phase difference detection circuit; and a phase control circuit for controlling the phase of a channel clock based on the output of the phase control data generation circuit. With this structure, it becomes possible to provide the phase correction circuit for a disk reproduction device which can generate a sampling clock even when there is a flaw, etc. in a fixed pattern area of the magneto-optical disk, by making a correction so as to avoid or reduce influence by the flaw, etc., and to provide the disk reproduction device using the circuit.

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